Docket No.: M4065.0100/P100-B

(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Howard E. Rhodes

Application No.: Not Yet Assigned Group Art Unit: N/A

Filed: Concurrently Herewith Examiner: Not Yet Assigned

For: CMOS IMAGER WITH SELECTIVELY

SILICIDED GATES

INFORMATION DISCLOSURE STATEMENT (IDS)

MS Patent Application Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 CFR 1.56, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO/SB/08. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement accompanies the new patent application submitted herewith.

A copy of each reference on PTO/SB/08 is attached.

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The Director is hereby authorized to charge any deficiency in the fees filed, asserted to be filed or which should have been filed herewith (or with any paper hereafter filed in this application by this firm) to our Deposit Account No. 04-1073, under Order No. M4065.0100/P100-B. A duplicate copy of this paper is enclosed.

Dated: July 14, 2003

Respectfully submitted,

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Substitute for form 1449/PTO				Complete if Known		
				Application Number	Not Yet Assigned	
11	NFORMATION	N DIS	SCLOSURE	Filing Date	Concurrently Herewith	
l s	STATEMENT I	BY A	APPLICANT	First Named Inventor	Howard E. Rhodes	
		•		Art Unit	N/A	
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Sheet	1	of	1	Attorney Docket Number	M4065.0100/P100-B	

	-		U.S. PA	TENT DOCUMENTS	
Examiner Initials*	Cite No.1	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant
IIIIIII	140.				Figures Appear
		4,374,700	02/1983	SCOTT et al.	
		5,319,604	06/1994_	IMONDI et al.	
		5,461,425	10/1995	FOWLER et al.	
		5,471,515	11/1995	FOSSUM et al.	
		5,541,402	07/1996	ACKLAND et al.	
		5,576,763	11/1996	ACKLAND et al.	
		5,612,799	03/1997	YAMAZAKI et al.	
		5,614,744	03/1997	MERRILL	
		5,625,210	04/1997	LEE et al.	
		5,705,846	01/1998	MERRILL	
		5,708,263	01/1998	WONG	
		5,739,562	04/1998	ACKLAND et al.	
		5,757,045	05/1998	TSAI et al.	

FOREIGN PATENT DOCUMENTS								
Examiner Initials*	Cite No.1	Foreign Patent Document Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶		

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NON PATENT LITERATURE DOCUMENTS						
Examiner Initials	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²			
		Dickinson, A., et al., <u>A 256x256 CMOS Active Pixel Image Sensor with Motion</u> <u>Detection</u> , 1995 IEEE International Solid-State Circuits Conference, pps. 226-227.				
i		Dickinson, A., et al., <u>Standard CMOS Active Pixel Image Sensors for Multimedia Applications</u> , Proceedings of Sixteenth Conference on Advanced Research in VLSI, March 27-29, 1995, pps. 214-224.				
, —		Eid, E-S., et al., <u>A 256 x 256 CMOS Active Pixel Image Sensor</u> , Proc. SPIE Vol. 2415, April 1995, pps. 265-275.				
		Fossum, E., CMOS Image Sensors: Electronic Camera On A Chip, 1995 IEEE, pps. 17-25.				
		Fossum, E., et al., IEDM A 37x28mm ² 600k-Pixel CMOS APS Dental X-Ray Camera- on-a-Chip with Self-Triggered Readout, 1998 IEEE International Solid-State Circuits				

PTO/SB/08a/b (05-03)
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	(use as many s	sheets as n	ecessary)	Examiner Name	Not Yet Assigned	
Sheet	2	of	1	Attorney Docket Number	M4065.0100/P100-B	

	Conference, pps. 172-173.
	Fossum, E., Low Power Camera-on-a-Chip Using CMOS Active Pixel Sensor
<u> </u>	<u>Technology</u> , 1995 IEEE, pps. 74-77.
	Fossum, E., Architectures for focal plane image processing, Optical Engineering, Vol.
1	28, No 8, August 1989, pps. 865-871.
	Janesick, J., et al., New advancements in charge-coupled device technology - sub-
	electron noise and 4096x4096 pixel CCDs, Proc. SPIE Vol. 1242, 1990, pps. 223-
]	237.
	Kemeny, S.E., et al., <u>Update on focal-plane image processing research</u> , Proc. SPIE
	Vol. 1447, 1991, pps. 243-250.
	Mendis, S., et al., CMOS Active Pixel Image Sensor, IEEE Transactions on Electron
	Devices, Vol. 41, No. 3, March 1994, pps. 452-453.
	
	Mendis, S.K., et al., A 128 x 128 CMOS Active Pixel Image Sensor for Highly
\vdash	Integrated Imaging Systems, 1993 IEEE, pps. 583-586.
1	Mendis, S.K., et al., CMOS Active Pixel Image Sensors for Highly Integrated Imaging
	Systems, IEEE Journal of Solid-State Circuits, Vol. 32, No. 2, February 1997, pps.
	187-197.
	Mendis, S.K., et al., <u>Design of a Low-Light-Level Image Sensor with On-Chip Sigma-</u>
 	Delta Analog-to-Digital Conversion, Proc. SPIE Vol. 1900, July 1993, pps. 31-39.
	Mendis, S.K., et al., Low-Light-Level Image Sensor with On-Chip Signal Processing,
	Proc. SPIE Vol. 1952, November 1993, pps. 23-33.
	Mendis, S.K., et al., <u>Progress In CMOS Active Pixel Image Sensors</u> , Proc. SPIE Vol.
	2172, May 1994, pps. 19-29.
	Nakamura, J., et al., CMOS Active Pixel Image Sensor with Simple Floating Gate
	Pixels, IEEE Transactions on Electron Devices, Vol. 42, No. 9, September 1995, pps.
	1693-1694.
ĺ	Nixon, R.H., et al., <u>256 x 256 CMOS Active Pixel Sensor Camera-on-a-Chip</u> , IEEE
	Journal of Solid-State Circuits, Vol. 31, No. 12, December 1996, pps. 2046-2050.
	Nixon, R.H., et al., <u>256x256 CMOS Active Pixel Sensor Camera-on-a-Chip</u> , 1996
	IEEE International Solid-State Circuits Conference, pps. 178-179.
	Panicacci, R., et al., <u>Programmable multiresolution CMOS active pixel sensor</u> , Proc.
	SPIE Vol. 2654, March 1996, pps. 72-79.
	Panicacci, R.A., et al., <u>128Mb/s Multiport CMOS Binary Active-Pixel Image Sensor</u> ,
	1996 IEEE International Solid-State Circuit Conference, pps. 100-101.
	Yadid-Pecht, O., et al., CMOS Active Pixel Sensor Star Tracker with Regional
	Electronic Shutter, IEEE Journal of Solid-State Circuits, Vol. 32, No. 2, February
	1997, pps. 285-288.
	Yadid-Pecht, O., et al., Wide dynamic range APS star tracker, Proc. SPIE Vol. 2654,
1	March 1996, pps. 82-92.
	Zarnowski, J., et al., Imaging options expand with CMOS technology, Laser Focus
	World, June 1997, pps. 125-130.
	Zhou, Z., et al., A Cmos Imager with On-Chip Variable Resolution for Light-Adaptive
	Imaging, 1998 IEEE International Solid-State Circuits Conference, pps. 174-175.
	Zhou, Z., et al., A Digital CMOS Active Pixel Image Sensor For Multimedia
	Lines, L., or all, A Digital Chief Process I feet image Densor I or inditinitedia

PTO/SB/08a/b (05-03)
Approved for use through 05/31/2003. OMB 0651-0031
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE
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Sheet	3	of	1	Attorney Docket Number	M4065.0100/P100-B	

I	Applications Date CDIT Vol. 2004 Contember 1006 ppg. 202 209	
<u> </u>	Applications, Proc. SPIE Vol. 2894, September 1996, pps. 282-288.	

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